

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: )  
)  
Tatsunori KANAI et al. )  
) Group Art Unit: Not assigned  
Application No.: Not Yet Assigned )  
) Examiner: Not assigned  
Filed: February 18, 2004 )  
)  
For: SERVER APPARATUS, KEY )  
MANAGEMENT APPARATUS, AND )  
ENCRYPTED COMMUNICATION )  
METHOD

**Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**

Sir:

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)**

Pursuant to 37 C.F.R. §§1.56 and 1.97(b), applicants bring to the Examiner's attention the document listed on attached Form PTO-1449. A copy of the listed document is attached. Applicants respectfully request that the Examiner consider the document listed on attached Form PTO-1449 and indicate that it was considered by making an appropriate notation on this form.

This Information Disclosure Statement is being filed with the above-referenced application.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that the listed document is material or constitutes "prior art." If the Examiner applies the document as prior art against any claim in the application and applicants determine that the cited document

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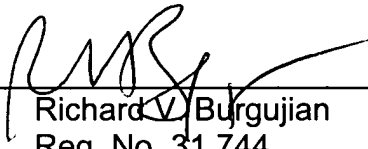
does not constitute "prior art" under United States law, applicants reserve the right to present to the Office the relevant facts and law regarding the appropriate status of such document. Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed document, should the document be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: February 18, 2004

By:   
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Enclosures  
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## INFORMATION DISCLOSURE CITATION

|                  |                        |                 |              |
|------------------|------------------------|-----------------|--------------|
| Atty. Docket No. | 04284.0880             | Application No. |              |
| Applicant        | Tatsunori KANAI et al. |                 |              |
| Filing Date      | February 18, 2004      | Group:          | Not assigned |

| U.S. PATENT DOCUMENTS |  |                 |            |      |       |           |                            |
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| Examiner Initial*     |  | Document Number | Issue Date | Name | Class | Sub Class | Filing Date If Appropriate |
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| FOREIGN PATENT DOCUMENTS |  |                 |                  |         |       |           |                       |
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| OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) |  |
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|  | Eric Rescorla, "Inline Hardware Accelerators", SSL and TLS, 6.21 (2001). |
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| Examiner  | Date Considered   |
| *Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. |   |
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